

Date – 04/12/2011

Attendees: CJ Clark, Bill Tuthill, Brian Turmelle, Carl Barnhart, Adam Cron, Carol Pyron, Craig Stephan, Ken Parker, Roland Latvala , Adam Ley, Wim Driessen, Francisco Russi, Ted Cleggett, John Braden,

Missing with pre-excuse Dave Dubberke,

Missing: Lee Whetsel, Neil Jacobson, Bill Eklow, Ted Eaton, Mike Richetti, Heiko Ehrenberg,

Agenda:

- 1) Patent Slides and Rules of Etiquette
- 2) *Clause 16 & 17*
 - a. Wrap-up
- 3) Clause 9 and 11
 - a. Discussion
- 4) Figure 6.1
- 5) PDL Annex C - Review Discussion

Meeting Called to order at 11:05 am EST

Minutes:

Review Patent Slide – Reminder sent out over email during the last week.
Review of Working Group Meeting Guidelines

Clause 16 Review

Carl has not received any feedback on the pages that he sent out to the group

Ken had sent an updated 16.1 picture

Changed 3 bubbled OR to an AND Gate

Carl – bubbles indicate active low and the function is an OR.

Table 16-1 shows logic hazards on dual transitions of reset – enable and reset control pairs.

Clause 17

17.3.1b – has not yet adopted recommendation to show which direction as to where each item gets documented. Either BSDL, datasheet, PDL, etc.

The term “datasheet” is not liked. Will replace with “specification”

Carl objected to recommendation because he thinks everything should be in published documentation

Carol disagrees.

Adam C: asks if it is in the PDL or BSDL does it need to be written somewhere else.

CJ: is it that if it is in one of 3 places is that good enough.

Carl: doesn't want to leave the impression that if it is in the BSDL it doesn't need to be written anywhere else.

Carol: any three is appropriate.

Adam L: doesn't think there is need to provided additional detail. if something needs to be noted . Doesn't think it is helpful to specifically direct which item goes in which document.

Carl: labeling everything bullet may imply that you don't need to document it other sources

CJ: General sense is not to take recommendation and keep section as is.
Section will be left as is without adding specific direction

Carl makes a motion to accept changes to date on clause 16 and 17 without prejudice to changes in the future.

Ken Seconds

Motion Passes Unanimously

Next sections to review for next meeting will be 9 and 11

Clause 9

Francisco volunteered to synthesize code snippets in clause 9

Ken recommends that code snippets should have figure number in them that they reference.

Carol: has some questions on figure 6-8.

Carl will update text and figure to clear up the questions that Carol had.

Clause 11

Section 11.8 Redundant Cells (ROOs)

This is where the rules are.

The rules for the Redundant Cells are in this section

Homework is to review Clause 9 and 11 for next meeting.

PDL

CJ wants to iron out the loading of the instruction register.

Two different methods

- 1) To use the -IR that is proposed with iApply
- 2) Use iWrite.instruction and then iApply.

With out the instruction switch on iApply you can also update data register with iApply and there is no order specified

Carol suggests making an example where the order does matter to highlight the problem.

Adam C: doesn't like that there is no order dependant and feels you will need to do more iApplys to make things happen in the sequence you expect.

CJ's concern is that the community is not used to the tools doing things behind the scenes

Ken : wants to know what the motivation is to have the tool do these loads behind the scenes.

CJ: Operating on a TDR and the instruction register is loaded one time.

Carl: objects to the PDL that CJ is showing. It does not make a strong connection between the instruction and the TDR

Carol : Reminds Carl that we looked into this earlier and thought it would be cumbersome when expanded out.

CJ: points out that the code example he has is not how he would write the code

CJ: 2 questions we need to asks are

- 1) Do we want to allow multiple behind the scenes instruction register scans?
- 2) What is the end user community comfortable?

Wim: would like a strict order to make different vendor tools act the same way when executing PDL

Wants to know how complex should we allow iApply to be

New Business

Francisco will submit his new business on line and it will get on the agenda for next week.

Meeting adjourned: 12:04 EST.

Next Meeting: 4/12/2011 11:00 AM EST

1 Motion Made

Motion to accept changes to date on clause 16 and 17 without prejudice to changes in the future.

Motion Passed Unanimously

NOTES:

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Meeting time: Tuesdays 11:00 AM (EST) (Recurring)

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-Computer Audio(Recommended)

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Current Issues listed and who will champion that issue.

1. Observe only. – Ken and Carl
1. Directionality linkage. - CJ
2. Power Pins. - Heiko
3. Pairing power pins with functional I/O - CJ
4. Sample / Capture. – Carol (Freescale) & Roland
5. TRST included in PCB level diagram. – Adam L.
6. Slow to Fall/Rise signaling issue – CJ
7. “No Connect” – Ken and Francisco.
8. Device ID – Still needs work
9. Low-Voltage self observe shorts coverage problem – JJ & Intel
10. Init – Carol & Carl

Action Items:

- CJ will post 1149.1 draft on website with line numbers to make it easier to refer to items in discussion
- Comment #10 CJ will take action to look at possibilities to add to the 1149.1WG website a document which shows which standards are based on 1149.1
- Comment #8 CJ will make changes to draft for observe only

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- Comment #7 CJ will get in touch with Doug to get input regarding Comments
- Comment #5 CJ will Add a figure and little text to address TRST use with interconnection of components
- Comment #4 Adam L to add comment about TRST. Update figure 6.8
- Comment #3 Adam L will update language for any proposed change for this section.