Date – 12/10/2010

Attendees:
CJ Clark,
Carl Barnhart,
John Braden,
Adam Ley,
Ken Parker,
Francisco Russi,
Heiko Ehrenberg,
Dave Dubberke,
Carol Pyron,
Roland Latvala

Agenda:
1) Discuss WG Elections
2) Review of IC-Reset Instruction
3) CPC documentation follow-up

Meeting Called to order at 11:30 am CST

Minutes:

WG Elections:
- CJ asked Carol to be the election vote counter and Carol agreed.
- Ken asked for nominations by Dec 14th and voting done by email thru Dec 21st. The WG members present agreed.
- This is a special case to allow Election voting by email due to the holidays. (It was noted that no technical topics are allowed to be voted on by email.) Nominations and elections will be for all WG leadership positions:
  - o Chairman
  - o Vice-Chairman
  - o Editor
  - o Secretary
- Francisco asked about consideration of the Tues and Friday meetings as separate from each other. CJ mentioned the Friday is just a tiger team for the Tues call.
- Roland asked about voting privileges for participants who can only attend the Friday meeting (ie: John and Roland). A motion was made and a vote taken to allow John and Roland rights to vote in the Election. The motion passed (6 yes votes and 1 abstain)

IC-Reset:
- Ken started the IC-Reset discussion with an overview from his prospective
- Carl mentioned this is the first instruction in the Std that is a system function, not a test function. "After it is invoked the test logic gets out of the way."
- Francisco thought that IC-Reset should only be a recommendation

- CJ mentioned that it will have rules, recommendations and permissions. It can be used in-situ for initializing on-chip test features by customers. Mbist, lbist,
- CJ mentioned the IC-Reset standardization will guide designers in the right direction. Master resets, local resets and so on.
- Question raised that IC-Reset should not reset any test logic, only system logic.
- John, Dave, and Roland agreed with the direction and motivation for IC-Reset.
- Adam and Heiko had nothing to add.
- CJ mentioned that TDRs and dedicated logic initialized by Init_Setup should also be preserved. Questions about whether or not this could be enforced or not. As this is the persistence question again, which was originally decided to be an optional feature. CJ thought this may need to be revisited too.
- Discussions about how long to stay in RTL state. Defined in TCK's and/or absolute time in the BSDL.
- Francisco asked about adding IC-Reset into the existing figure 6.8 for POR. CJ said we will discuss that later after IC-Reset is better defined.
- Carl and Ken collaborated on the following bullet list for IC-Reset behavior.
- This list forwarded from Ken pasted inline here:

Ken's IC-Reset bullet list from today's discussion:
- IC_RESET is a TAP-based way of asserting RESET even when the Mode Line disconnects the RESET pin from the system logic.
   - There is a TDR.
   - TDR LSB is "master reset", '1' specifies "execute master reset", "0" (default) says no master reset
          - How is default set? TRST*, TLR, Update-IR of IC_RESET, etc?
   - Upper bits are allowed to subset reset as designer wishes (with LSB 0)
   - TCKs in RTI used to drive state machine of Reset process
   - Entering RTI starts process (like asserting RESET pin), Exit RTI is same as de-asserting the RESET pin
   - Run spec: Absolute time (for discharging things), TCK (for clocking state machines)
   - Post RTI latency (how manyTCKs after de-assertion) Also, time or sys clocks?

- IC_RESET does not affect the Clamp-Persistence state. Implications:
   - if in Clamp-Persistence, it only resets internal system logic
   - if not in Clamp-Persistence, all I/O also respond to reset processing.

- IC_RESET does not affect the INIT_DATA register content. (Is this tied to persistence??)
   - if in Clamp-Persistence, xxxxx
   - if not in Clamp-Persistence, yyyyy

- Action item taken by Carl is to use this list above as basis for writing up a straw-man set of rules for IC-Reset

CPC documentation:
- Near close of the meeting CJ asked Adam Ley about his position on how the CPC was incorporated into the Std. Adam's position hasn't changed but he felt if others were ok with current plan then that is the way it should be. CJ wanted to accommodate everyone if possible and use a vote as a last resort when consensus was not achieved.

Meeting adjourned: 1:00pm EST.

Action Items:
- Action item taken by Carl to write up a straw-man set of rules for IC-Reset

Next Tiger Team Meeting: Currently planned for next Friday 12/17/2010 11:30 AM EST