

Date – 05/12/2014

Attendees: CJ Clark, Adam Ley, Bill Tuthill, Bob Gottlieb, Brian Turmelle, Craig Stephan, Dharma Konda, Frans de Jong, Gobinathan Athimolom, Jon Colburn, Josh Ferry, Marc Hutner, Philippe Lebourg, Roger Sowada, Tapan J Chakraborty, Zahi Abuhanmdeh

**Absent with Excuse :,
Not Present for ¾ of meeting:**

Missing: Bill Huott, Carol Pyron, Jim Wilson, Kent Ng, Kevin Gorman, Saman Adham, Tom Waayers, Heiko Ehrenburg, Gurgun Harutyunyan, Dwayne Burek, Dave Armstrong, Ismed Hartanto, Mike Ricchetti, Steve Sunter, Teresa McLaurin,

Agenda:

- 1) Patent Slide
- 2) Clause 4 addition of Fran's state machine
 - a. Motion to approve?
- 3) Clause 5 homework review
 - a. Any further input? Is the ordering of the bits clear, understandable and reproducible?
- 4) [New Business](#)

Meeting Called to order at 11:05 am EDT

Minutes:

Review Patent Slide – Slide Presented to the Group.

Solicited input from anybody who is aware of patents that might read on our standard.

No Response

Addition of Fran's State Machine to Clause 4

Bob sent a few issues over the reflector

What is the default path?

CJ – Rule 4.1.2 c goes into it

If you don't use a TAP than PEDDA should be configured to get packet.

Bob – this would be a dedicated pin interface only for this?

Was stuck on how to over load something on this. But this is a dedicated pin set for HSJTAG

Jon – default arc is dedicated use

CJ – will add “dedicated” to the State Machine Diagram for the default path

Philippe – What is dedicated here

CJ – HSJTAG Tap not shared with Mission mode.

No compliance enable pins.

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Philippe – if there is no mission mode why go into mission mode state on system reset

CJ – mission mode is referring to the case that has the 1149.10 TAP on it

Philippe – Dedicated Interface would be clearer

CJ- ok will make that change.

Frans – it was intended to be state diagram. Circles are states and arrows are reactions to conditions. Default (dedicated) is based on action receiving config packet. If it is interface than you define interface as being an action.

Philippe- could state Dedicated Interface Recognized.

Frans – Tap needed should be “Tap Used”

CJ updated diagram.

Philippe – Tap Used is dot1 tap?

Frans – yes.

Philippe – are the actions commented in the text?

CJ – yes. There are descriptions in clause 4

Frans – Could be added that the state drawing reflects rule c

Philippe - Motion to accept Clause 4 subject to future editorial changes.

Jon – Seconded

No further discussion.

Yes (12)

Bill T.	Craig S.	Gobi	Marc H.
Brian T.	Dharma K	Joh C	Roger S.
Bob G.	Frans	Josh F	Tapan

No (0)

Abstain (1)

Adam L.

Motion passes

12 Yes

0 No

1 Abstain

Clause 5 review

CJ added updates based on last week’s feedback

Bob – when we have a config that setups up target ID, did we decide the only way to reset that with a power cycle? Does a system reset also clear the target? Would like a way other than power cycling to set the target back to 0

CJ – can be what we want it to be. A system reset brings us out of the state machine. For a dedicated interface would make sense to have a system reset also clear it.

Bob – not clear what we should do in the design.

CJ updated rule i to add or system reset

CJ – how about a packet to clear it?

Bob – that would be good.

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CJ – bothered by system reset a little. With the TAP you yank on TRST or enter TLR state on power up. If in TLR state that issues a reset. Would be better off if our config register takes the RESET***, if you have a TAP at power on it would be available.

Bob – didn't want to do it as part of a TAP Reset, we didn't want to reset the whole system if we go through the reset state.

Bob – what reset to use?

CJ - was thinking of a POR. Can do POR OR'd with another signal.

Bob – if POR is a signal and not an indication that we power cycled than ok with that.

Frans – don't see any problem with combining POR and register signal. To define that you are out of testing and back to mission mode.

CJ – not sure bob is saying it is changing dot 10 mode. Just clearing config register

Bob – clearing the config register.

Frans – make it compatible with the flows we have.

Bob – left arc system reset that takes us back up to mission mode.

Frans – can define down the road that there are registers not affected by reset. System reset goes back to mission mode.

Bob – wasn't clear if that System Reset clears config.

CJ – if you lose configuration it doesn't seem like it would take a long time to get back to communicate after system reset.

Maybe we put a block on there. An LSB bit that is used to prevent a register from being reset. Can hold configuration of TAP through reset so configuration is not lost.

Frans - Rule b signals – is h Enable_1149.10 a TAP signal?

CJ – should we remove the word TAP and just have it say interface?

Frans – no not necessarily.

CJ – needs this signal as an input.

Frans – will read it again and respond back on the reflector.

CJ – the Word TAP might need to be removed.

Call for New Business

No new business

Please use reflector to review what is in the Draft.

Motion to Adjourn: Bill

Seconded: Frans

Meeting adjourned: 12:00 pm EDT

Next Meeting:

May 19th, 2014 11:00am

Motion Summary

1 motions made

Motion to accept Clause 4 subject to future editorial changes.

Motion Passed 12/0/1

Action Items

~~Bill Tuthill—10-21-2013—Add minutes and Attendance spreadsheet to the website.~~

~~CJ—11-11-2013—Reach out to ATE industry and Probe Industry to get update on future of ATE equipment to see which data speeds and protocols they are heading towards.~~

Philippe – Look into alternative method to create control information (pause, start, terminate, etc.) rather than using K characters in packet.

Bob – create a case study to show use of Attributes

Frans – will start some block diagrams of a simple use case to help illustrate the current architecture

~~Dwayne—present to the group his ideas for a simplified scheme—Direct Interface.~~

NOTES:

1149.10 working group website - <http://grouper.ieee.org/groups/1149/10/>

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